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# Wavelength dependent photosensitivity modulation of Aluminium/Lead sulphide/Indium tin oxide back-to-back diode

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#### Abstract

The photosensitivity of aluminium (AI)/lead sulphide (PbS)/indium tin oxide (ITO) thin layered structure is investigated considering the photon wavelength dependent current-voltage and capacitance-voltage characteristics of the device. The current-voltage characteristics of the test structure are analyzed adopting the back-to-back Schottky barrier diode model. The diode possesses low dark current in contrast to the high value of photocurrents measured under different illumination wavelengths. The change in photo-sensitivity of the device. The capacitance-voltage characteristics of AI/PbS/ITO structure demonstrate a definite improvement of the device capacitance with the higher wavelength exposures. The matter is explained in terms of the additional capacitance owing to the excess carrier generation within the device under illumination. The photosensitivity modulation of the device can be exploited in photo-sensor or photo-detector applications in various electronic devices.

**Keywords**: Aluminium/lead sulphide/indium tin oxide, Back-to-back diode, Current-voltage, Capacitance-voltage, Photo-sensitivity.

### Introduction

The electrical and optoelectronic properties of a variety of metal-semiconductor-metal (M-S-M) back-to-back diode structures are studied in numerous occasions earlier (Chu et al., 2005; Pandit and Cho, 2018; Khalli and Debbar, 2019; Ruzgar and Caglar, 2020; Averin et al., 2020). Majority of these studies have mainly investigated the photosensitivity of the device in terms of the improvement of diode current and responsivity. However, to assess the quality of the photodiode, the role of photon wavelength dependent photocarrier generation and subsequent transportation through the device requires added attention and need to be studied in a more accurate manner. In fact, the reflection from the semiconductor surface and surface metal, the finite photo-generated carrier lifetime, the rate of recombination in the surface and deep traps within the semiconductor are largely dependent on the photon wavelength, which eventually controls the quality of the photodiode in terms of the device photosensitivity.

In the present work, the photon wavelength dependent photosensitivity modulation of a metal-semiconductor-metal configuration is investigated considering the aluminium/lead sulphide/ indium tin oxide structure and the role of exposure wavelength on the currentvoltage and capacitance-voltage characteristics of the device are analyzed. Lead sulphide (PbS) is a direct narrow band gap IV-VI semiconductor ( $E_g \approx 0.41$  at 300 K for bulk crystal) (Banerjee, 2019). The electrical and optoelectronic properties of PbS nanostructures and thin films are studied extensively time to time and subsequently employed in different semiconductor devices, namely the infrared detector (Ghamsari et al., 2006), solar cell (Gunes et al., 2007), Pb<sup>2+</sup> ion selective sensors (Hirata and Higashiyama, 1971), photo-resistor (Miroshnikova, 2010), humidity and temperature sensors (Pop et al., 1997; Seghaier et al., 2006). In the present work, the photon wavelength dependent photosensitivity of the Al/PbS/ITO M-S-M structure is investigated considering back-toback Schottky barrier diode model. The results of such investigations are reasonably interesting and discussed in the following sections.

#### **Materials and Methods**

A 20mm×15mm×2mm ITO coated glass slide is cleaned ultrasonically in distilled water, trichloroethylene, acetone and methanol, respectively. To grow the PbS film over the ITO coated surface, the uncontaminated slide is dipped vertically into a reactive mixture of 0.2 M aqueous solution of lead nitrate  $[Pb(NO_3)_2]$  with 0.1 M aqueous solution of thiourea  $[SC(NH_2)_2]$ . The alkalinity of the solution is set by 0.6M aqueous solution of sodium hydroxide [NaOH]. The solution is stirred continuously within 100-120 rpm keeping the solution temperature within 35-40°C. The slide is removed from the solution after 30 min, dried at room temperature and subsequently placed into a stack furnace and annealed at 400 K for 20 min. The homogeneity of the deposited PbS film is primarily monitored by a metallurgical microscope (Banbros: BMU 101). The average film thickness of the deposited PbS layer is measured 20µm using a digital electronic micrometer (Toolsden DM0251). The FESEM surface morphology of the PbS film is shown in fig.1.



Figure 1. FESEM surface micrographs of chemical bath deposited PbS film in two different scales.

Two identical aluminium contact leads are bonded 2 mm apart using silver epoxy [Alfa Aesar (42469); sheet resistance < 0.025  $\Omega$ /sq (0.001 in. thick)], one over the exposed ITO layer (back electrode) and other on the PbS deposit (top electrode). The scheme is illustrated in Fig 2. All the current (*I*)-voltage (*V*) measurements in dark and illuminated conditions are performed using Keithley 2401 source measurement unit. The optoelectronic *I-V* measurements are carried out under 510-550 nm, 585-605 nm and 620-640 nm of continuous illumination. Additionally, the capacitance (*C*)-voltage (*V*) measurements of the device are carried out under both dark and steady illuminations (same set of wavelengths used for dc optoelectronic measurements) using SM6020 LCR meter. All the measurements are carried out at room temperature (~300 K).



Figure 2. Schematic of aluminium/ lead sulphide/ indium tin oxide organic diode with metal contacts as electrodes.

## **Results and discussions**

The absorbance plot of the PbS film clearly indicates that the PbS structure well absorbs the photon nearly 530 nm. Fig.3 shows the absorbance as well as the transmittance (%) plot of the deposited PbS film. The minor scattering and some other inconsequential peaks observed in the absorbance plot are attributed to the lattice resonance and subband transitions related to the deposited material. The measured forward and reverse current-voltage characteristics of the Al/PbS/ITO diode are shown in fig. 4. The plot shows a definite improvement in the forward current, when ITO contact (back contact) is



Figure 3. Normalized absorbance vs. wavelength plot of PbS thin film. Inset (A) demonstrates the transmittance (%) vs. wavelength plot of the same deposited film.



Figure 4. Forward and reverse current-voltage characteristics of the AI/PbS/ITO diode.

kept at lower bias condition with respect to Al contact over PbS layer (top contact) and when the applied bias is more than  $\pm 0.2V$ , the forward to reverse current ratio is found to be

nearly 10<sup>2</sup>. The fact is encouraging to apply the device in case of precision rectification in different semiconductor devices. Moreover, the device is found to be largely photosensitive. The fact is elucidated during the time of current-voltage measurements, when the diode is exposed under a set of different photon wavelengths.



Figure 5. Forward dark current and photocurrent under three different ranges of exposure wavelengths (510-550 nm, 585-605 nm and 620-640 nm).

During steady illumination, the diode possesses at least 80-100 times higher forward current, in contrast to the dark current. As the wavelength increases, the diode forward current increases significantly. The matter is shown in fig. 5, where the measured forward current under three different wavelength regions (510-550 nm, 585-605 nm and 620-640 nm) are plotted along with the measured dark current. The photon wavelength dependent deviation of the device current can be explained considering the excess photocurrent generation due to absorption of photon energy (hv), higher than the transition energy of the material. In such situation, the photocurrent density in the semiconductor layer of device can be expressed as (Banerjee, 2019)-

$$J_{photo} = \left(\frac{P_{opt}}{h\nu}\right) \frac{\eta \tau q}{A} \left(\frac{1}{t_e} + \frac{1}{t_h}\right)$$
(1)

where  $P_{opt}$  is the incident optical power,  $\tau$  is the excess carrier recombination time,  $\eta$  is the quantum efficiency and A is the area. The parameters  $t_e$  and  $t_h$  are the transit time of electrons and holes within the semiconductor.

The voltage dependent device current of the test structure is analyzed considering the back-to-back diode model of Schottky barrier diode and in such case the generalized form of the current-voltage relation is given by (Nouchi, 2014; Osvald, 2015)-

$$I = \frac{2\frac{I_{01}}{A_1}\frac{I_{02}}{A_2}Sinh\left(\frac{qV}{2nkT}\right)}{\frac{I_{01}}{A_1}\exp\left(-\frac{qV}{2nkT}\right) + \frac{I_{02}}{A_2}\exp\left(\frac{qV}{2nkT}\right)}$$
(2)

where  $I_{01}$  and  $I_{02}$  are the reverse saturation currents of the two asymmetric diodes and can be expressed as

$$I_{01} = A_1 A^* T^2 \exp\left(-\frac{q\phi_{B1}}{kT}\right)$$
(3)

and

$$I_{02} = A_2 A^* T^2 \exp\left(-\frac{q\phi_{B2}}{kT}\right)$$
(4)

where  $\phi_{B1}$  and  $\phi_{B2}$  are the two asymmetric barrier heights.  $A^*$  is the Richardson constant,  $A_1$  and  $A_2$  are the effective junction area of the two diodes. However, this type of metalsemiconductor-metal devices can suffer from a large amount of series resistance. In presence of series resistance, the voltage across the two terminals of the device is expressed in terms of device current (I) as (Osvald, 2015)-

$$V = V_1 + V_2 + IR_s \tag{5}$$

$$V = \frac{n_1 kT}{q} \ln\left(\frac{I}{I_{01}} + 1\right) - \frac{n_2 kT}{q} \ln\left(-\frac{I}{I_{02}} + 1\right) + IR_s$$
(6)

To assess the nature of two major device parameters, namely, the dynamic resistance and the conductance of this device, the voltage variation of the dark current is used to estimate the parameters numerically. Fig. 6 shows the voltage dependence of the dynamic resistance and conductance of the device. The voltage variation of the dynamic resistance has shown a decreasing trend with the development of applied bias. The fact is well endorsed by the conductance plot of the device, where the parameter has shown an increasing trend with the applied bias.



Figure 6. Voltage variation of dynamic resistance and conductance of Al/PbS/ITO diode. The parameters are extracted numerically from the measured dark current of the device.

In the ac analyses, the capacitance-voltage characteristics of the AI/PbS/ITO structure are investigated considering both dark and illuminated conditions. Fig.7 shows the variation of capacitance within the voltage range of ±0.4V. Interestingly, a distinct divergence in the device capacitance is observed under a variety of illumination wavelengths. The nature of such capacitancevoltage characteristics clearly substantiates the role of photon wavelength on the origin of excess capacitance in addition to the dark capacitance. During illumination the photogenerated carriers contribute to the total capacitance of the device, ensuing the improvement of the capacitance. The wavelength dependent capacitance-voltage characteristics can be utilized in a variety of semiconductor optoelectronic devices.



Figure 7. The capacitance-voltage characteristics of AI/PbS/ITO diode for (A) AI +ve and ITO –ve and (B) AI –ve and ITO +ve bias conditions.

### Conclusion

In conclusion, an Al/PbS/ITO diode is fabricated in the present work, by chemical

bath deposition technique. Different electrical and optoelectronic properties of the diode are investigated experimentally. The device is found to be a good photo-sensor, as both the current-voltage and capacitance-voltage characteristics are largely influenced by the illumination wavelength. The diode can be used as a photo-sensor or photo-capacitor device in different optoelectronic applications.

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