



An Efficient PV - Integrated UPQC System for Power Quality Enhancement Using Improved Chicken Swarm Optimization

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Abstract: In today's world, the power distribution system's problems with Power Quality (PQ) are not unfamiliar. However, customer awareness of these issues has also risen. Likewise, there are many conventional solutions to improve the power quality. However, these traditional solutions rely on passive elements and frequently fail to respond effectively as the nature of the power system changes. Custom power devices provide adaptable solutions for many issues. Therefore, a Unified Power Quality Conditioner (UPQC) Coupled with a PV system is proposed in this paper to improve the PQ problems, including fluctuations, voltage sag and swell etc. The deployment of an Interleaved Zeta converter aids in maintaining the constant DC link voltage. In order to derive the maximum desirable power from PV system, STATCOM with Adaptive Neuro- Fuzzy Interference System (ANFIS) is used, which is fine-tuned with the help of the Improved Chicken Swarm (ICS) algorithm. In addition, UPQC with series and shunt converter is employed to decrease the current and voltage harmonics, thereby enhancing the power factor. For controlling the working of UPQC, the d-q theory-based Proportional Integral (PI) is proposed. The suggested design of PV-UPQC is evaluated and the simulation results are obtained in the MATLAB platform with a reduced THD of 2.01%.

Introduction

Increasing the amount of Renewable Energy Sources (RES) penetration in the utility phase has become necessary for utilities due to the rise in electricity consumption, the need to preserve the environment, and the depletion of fossil fuel reserves. Because of their nature and distribution penetration, mitigating environmental pollution, renewable energy can reduce power losses, reduce operating costs, eliminate system upgrades and improve voltage stability. By injecting the RES into the utility grid, the power quality decreases and generates PQ issues (Chawda et al., 2020; Basit et al., 2020). These issues include reactive power flow, voltage fluctuations, excessive neutral current and harmonics etc. Some other problems regarding the voltage side distribution system and RES placement are also discussed

by Chang and Chinh (2020). Using a Flexible AC Transmission System (FACTS) largely overcomes these PQ concerns (Khan et al., 2021). The various types of FACT devices mentioned in (Sindi et al., 2023), including Thyristor Controlled Series Compensator (TCSC) controllers, are utilized to improve the quantity of the control flow of active power and transmission lines in electric systems. The Static Var Compensator (SVC) is more effective at supporting the DC voltage and keeps the power supply reliable and efficient (Santika et al., 2018). Static Synchronous Compensator (STATCOM) is an active shunt circuit that improves voltage regulation, power flow, and transmission line stability when connected at the midpoint (Abdelaziz et al., 2020). D-STATCOM is another device which is used for harmonic mitigation and power factor improvement (Saedinia et



al., 2022). The distorted voltage is reduced and thus, the power quality improves. The Dynamic Voltage Restorer (DVR) efficiently identifies and rectifies voltage sags in the AC power source by insulating the loads from these power reliability problems (Babu et al., 2021). However, the methods mentioned earlier mostly depend on the control approaches (Babu et al., 2019), which have high power losses. Hence, the proposed PV-UPQC efficiently improves the PQ and grid performance with minimized THD.

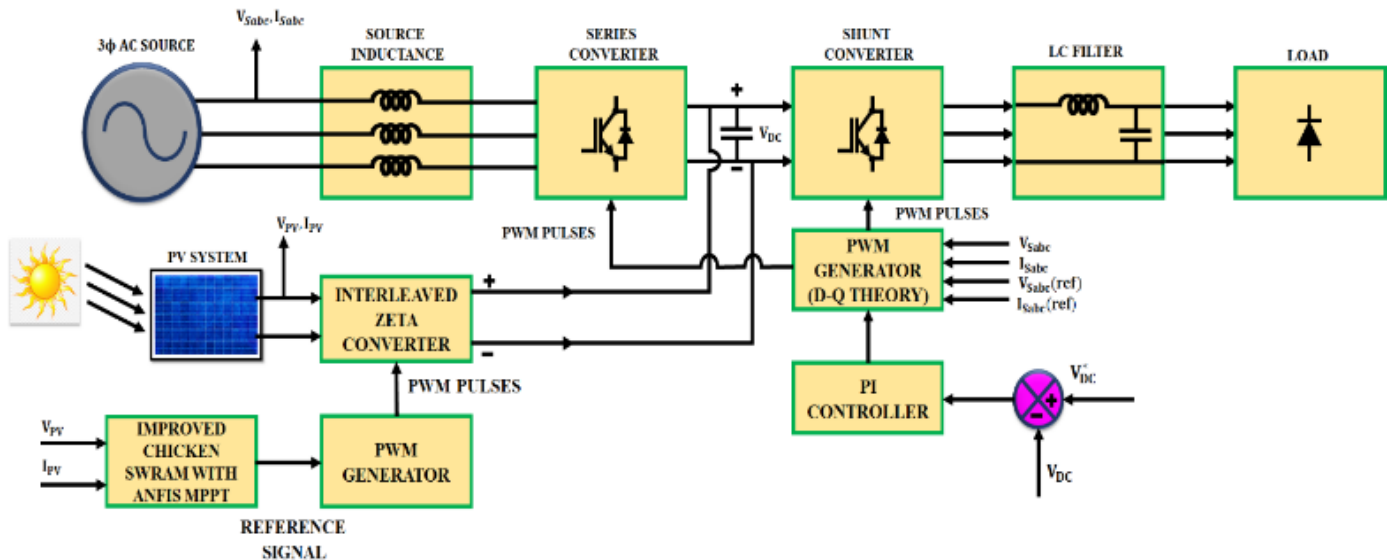


Figure 1. PV-UQPC system configuration

The PV systems are employed to produce the electricity Babu et al. (2021a) to fulfil the load demand. Natural resources could be used to reduce energy consumption. However, a PV array provides a better result, but the output voltage is extremely low (Manikandan et al., 2016; Kumar et al., 2022). Specific types of boost power circuits can control the output voltage. For low power loads, the Boost converter is insufficient. To beat the demerits of the Buck and Boost converter, the combination of Buck-Boost is introduced (Sathish et al., 2022). It is used in industries to step up and down operations. The converter achieves maximum efficiency depending on the conditions and operating modes (Babu et al., 2021b). However, a more comprehensive range of DC input voltage influences the efficiency and generates the ripples.

Moreover, the voltage lift approach-based Luo converter (Reddy et al., 2022a) and SEPIC (Reddy et al., 2022b) enhance PV outputs. However, these converter exhibits issues related to output gains. A non-inverting DC output voltage characterizes the fourth-order Zeta converter (Sathish et al., 2022). But still, due to the high switching losses, the system fails to regulate the gain. The suggested interleaved zeta converter (Sathish et al., 2023) has high efficiency, low input current distortion

and less output voltage ripple.

MPPT methods deployed to determine the maximum energy possible from PV systems, have grown in popularity over the last decades. Traditional MPPTs, including incremental conductance and perturb and observe (P & O), are known to be more effective for uniform irradiance with a single peak (Balakishan et al., 2023). Nonetheless, while the system is exposed to Partial Shading (PS) circumstances, it is not applicable and power loss occurs as a result of the algorithm being

frequently trapped at a local peak. The Proposed ANFIS extracts the maximum power from PV under PS conditions (Quawi et al., 2023) and the parameters of ANFIS are tuned optimally with the help of the ICS algorithm. This presented approach is effective when compared to other metaheuristic algorithms including Particle Swarm Optimization (PSO) (Rudram et al., 2023), Whale Optimization (Thota et al., 2023) and Genetic Algorithm (GA) (Abdul et al., 2020).

Therefore, this work proposes a PV integrated with the UPQC system to enhance the PQ issues. The UPQC and series and shunt converter are used to obtain a stabilized output with reduced current harmonics. The proposed ANFIS, tuned with the assistance of ICS, efficiently tracks the utmost power from the PV system. The STATCOM-UQPC is controlled by a PI controller based on the d-q theory. The proposed work is verified using MATLAB simulation.

Proposed System

The control and design of PV-UQPC are suggested in this work. The UQPC Comprises a shunt and series compensator, which is represented in Figure 1.

A PV-coupled UPQC's primary function is to compensate for PQ issues of the supply voltage, such as sags, unbalance, swells, flicker and harmonics, and load current power quality issues. The Interleaved Zeta converter maintains the stable DC link voltage with reduced ripples. To maximise energy extraction from PV, the PV output is fed into an ICS ANFIS-based MPPT approach, efficiently separating the highest power from PV. By generating gating pulses, the PWM generator regulates the operation of the converter. The converter's controlled outcome of the ideal voltage level is injected into the DC link. The dq theory-based PI controller is employed to control the working of the shunt and series converter of UPQC. As a result, the PQ issues are mitigated with an improved power factor.

Proposed System Modelling

Modelling of PV System

The PV cells are one of the primary components of solar power plants, which convert solar power into electrical energy. Figure 2 depicts the equivalent circuit of a PV cell and its various components. In practical, the resistance R_{sh} and R_s are not included in the system.

The PV cell's output current is

$$I_{PV} = I_L - I_{do} - I_{sh} \dots \dots \dots (1)$$

The diode current equation can be written as,

$$I_{do} = I_o \left\{ \exp \left(\frac{qV_d}{\eta kT} \right) - 1 \right\} \dots \dots \dots (2)$$

The P-V characteristic of the common equation is:

$$I_{PV} = I_L - I_o \left[\exp \left(\frac{q(V_{PV} + IR_s)}{\eta kT} \right) - 1 \right] - \left[V_{PV} + \frac{IR_s}{R_{sh}} \right] \dots \dots \dots (3)$$

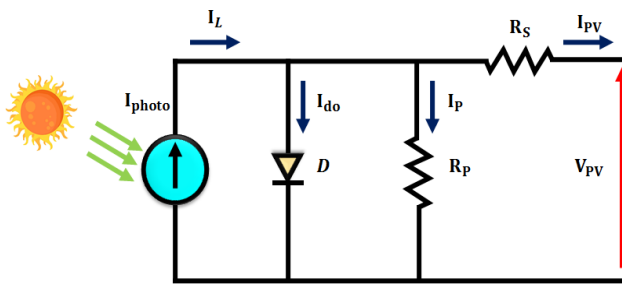


Figure 2. PV Cell

Solar cells can be connected in series or parallel to obtain the desired voltage and current. Furthermore, different connection states result in different photovoltaic array topologies. The output of PV is severely low. To boost the output voltage and current of PV, the interleaved zeta converter is introduced and it also has high efficiency with a wide range duty ratio.

Modelling of Interleaved Zeta Converter

The traditional converters are frequently used to decrease the DC supply with poor efficiency. However, the conventional Zeta converter is inappropriate for

wider-range operations due to its lower efficacy. So, the Interleaved Zeta converter is presented in this work. The Interleaved Zeta Converter's circuit diagram is depicted in Figure 3.

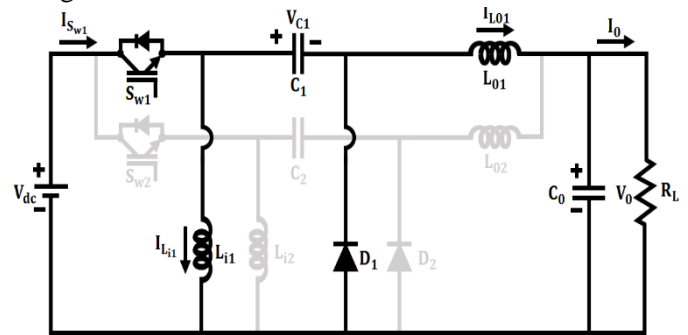


Figure 3. Interleaved Zeta converter circuit diagram

Stage 1 [t₀ ≤ t ≤ t₁]:

The capacitor C₁ and inductor L_{i1} store energy from the DC source V_{dc}. Switch S₁ is switched on during Stage 1, while S₂ is deactivated at the same moment. The output DC connection capacitor C₀ transfers the inductor L_{o1} stored energy to the load. During this mode, the inductors L_{o1} and C₀ are resonating.

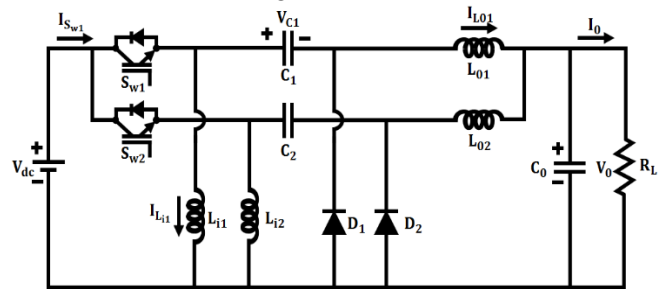


Figure 3. (a). Stage 1

Stage 2 [t₁ ≤ t ≤ t₂]:

While in this stage 2, the switches S₁ and S₂ are in ON state. Through the output capacitor C₀, the inductor L_{o1} releases energy to the load. Through the switch S₂ the inductor L_{i2} and C₂ begin charging from the DC source. Towards the end of this mode, the fully charged inductors L_{i1} begin to discharge.

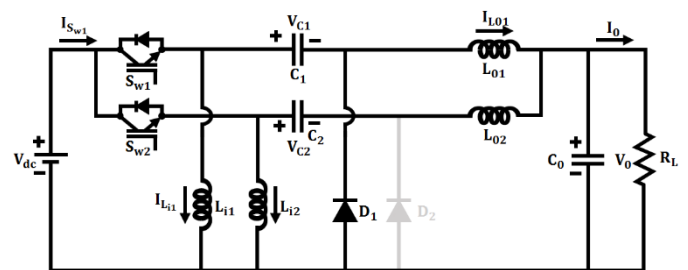


Figure 3(b). Stage 2

Stage 3 [t₂ ≤ t ≤ t₃]:

In Stage 3, S₁ is switched off while S₂ is maintained in the on position. Until the switch is turned on, the inductor L_{i2} and C₂ are continuously charging. Through the DC connection capacitor, the inductors L_{i1} and L_{o1} start discharging to the load. By the end of this phase, L_{o1} is

fully discharged, charging in the opposite direction, and the output capacitor and load receive the least current.

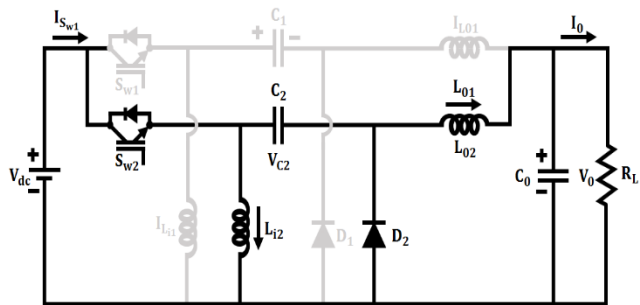


Figure 3(c) Stage 3

Stage 4 [$t_3 \leq t \leq t_4$]:

The S_1 and S_2 switches are both switched ON. Through the DC connection capacitor, the inductors L_{i2} , L_{o2} start discharging to the load. Through the switch S_1 , the inductors L_{i1} , L_{o1} and capacitor C_1 are storing energy from the DC source V_{dc} . The L_{i2} , L_{o2} inductors entirely discharged at the mode's end, and the cycle repeated.

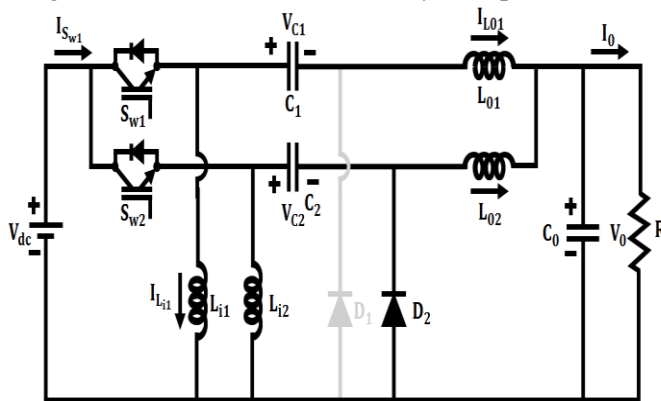


Figure 3(d) Stage 4

The Interleaved Zeta converter ensures optimal power transfer by using an improved CS with ANFIS MPPT controller. The fourth coming section briefly explains suggested ICS-assisted ANFIS MPPT.

Modelling of Improved Chicken Swaram Algorithm with ANFIS MPPT

ANFIS-MPPT

The ANFIS incorporates fuzzy logic with Artificial Neural Network (ANN). An ANN is used to train the new fuzzy controller to determine the variable-based membership function. A comprehensive foundation rule is created by deriving the weight of each node involved (Ahmad et al., 2021). The membership function and rule table can be easily tuned with the aid of the ANN. The ANFIS (Anand et al., 2018) controller's inference system corresponds to a collection of fitness values to optimise nonlinear functions. A two-input (x,y)-one output fuzzy rule set (z) One way to give FIS is as follows:

The first rule states that if x and y are both A_1 and B_1 , then

$$f_1 = p_1x + q_1y + r_1 \dots \dots \dots (4)$$

The second rule states that if x and y are both A_2 and B_2 , then

$$f_2 = p_2x + q_2y + r_2 \dots \dots \dots (5)$$

The following equation (6) represents the function of output:

$$f = \frac{\omega_1 f_1 + \omega_2 f_2}{\omega_1 + \omega_2} = \bar{\omega}_1 f_1 + \bar{\omega}_2 f_2 \dots \dots \dots (6)$$

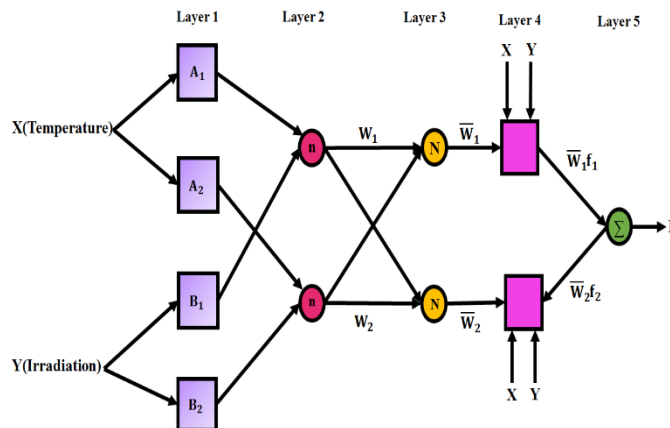


Figure 4. ANFIS based MPPT

Figure 4 depicts the ANFIS's design with two inputs (x, y) and one output (z).

Layer 1

Usually, most nodes are flexible and node i and O_1 dependent on the input of the membership functions with corresponding node i for its output.

$$\begin{aligned} O_{1,i} &= \mu_{A_i}(x), & \text{For } i=1,2 & \dots \dots \dots (7) \\ O_{1,i} &= \mu_{B_i}(y), & \text{For } i=3,4 & \end{aligned}$$

Here, the inputs are x and y, and the corresponding A_i and B_i fuzzy sets are parametric sets of fuzzy sets i. Gaussian membership functions are used for the inputs x and y in this work.

Layer 2

Here, the nodes are unchangeable and output i is the outcome of its input functions. The term "neural network layer" refers to this layer since it multipliers.

$$O_{2,i} = \omega_i = \mu_{A_i}(x) * \mu_{B_i}(x) \quad \text{For } i = 1,2 \dots \dots \dots (8)$$

Layer 3

The N fixes and uniquely identifies each node. The output of layer 3 (O_3) i is referred to as having standardised firing strengths from the layer before that since it is the sum of the firing strengths of the rules.

$$O_{3,i} = \bar{\omega}_i = \frac{\omega_i}{\omega_1 + \omega_2}, \quad \text{For } i = 1,2 \dots \dots \dots (9)$$

Layer 4

The parameters follow naturally from the changeable node features. This fuzzy logic node has the parameter values p_i, q_i and r_i equation displays the node's output (10)

$$O_{4,i} = \bar{\omega}_i f_i = \bar{\omega}_i (p_i x + q_i y + r_i), \text{ For } i = 1,2 \dots (10)$$

the larger φ_2 , the chick is more likely to find the rooster, which is a local search close to the optimal solution. This Paper implements a nonlinear learning component, enabling local and global searches for chicks. The equation is as follows:

$x_{r1,j}(t)$ and $x_{r2,j}(t)$, the mutation operation is carried out by the following equation.

$$V_{i,j}(t + 1) = x_{i,j}(t) + F * (x_{r1,j}(t) - x_{r2,j}(t)) \dots (19)$$

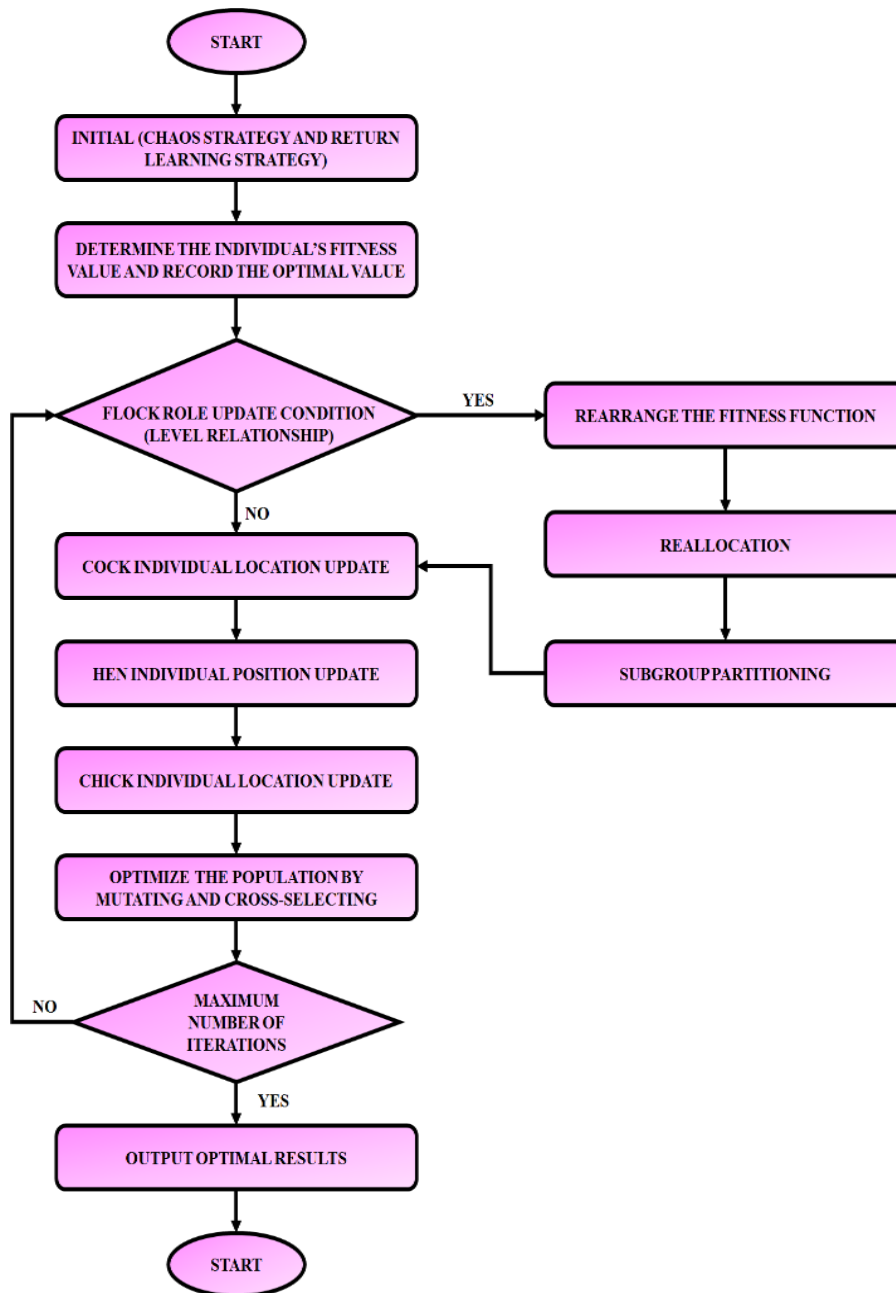


Figure 5. Flow chart of ICS Algorithm

$$\varphi_1 = 1.3 + 1.2 * \cos(t\pi/T_{max}), \dots (17)$$

$$\varphi_2 = 2 - 1.2 * \cos(t\pi/T_{max}) \dots (18)$$

Select the Best Individual

A differential method that consists of the three stages of variation, intersection, and selection is utilised to choose the best location for the chicken swarm optimisation. The important equation is as follows:

(1) Variation Procedure: Following a random selection of two identical-iteration-number bodies,

Where F is a random factor which regulates the extent of growth of the score vector and $V_{i,j}(t + 1)$ is the mutant individual.

(2) Cross Procedure: Indicator of cross-probability P is added, and the cross operation is carried out using the equation below:

$$k_{i,j}(t) = \begin{cases} v_{i,j}(t + 1), & P \in [0,1], \\ x_{i,j}(t), & \text{otherwise} \end{cases} \dots (20)$$

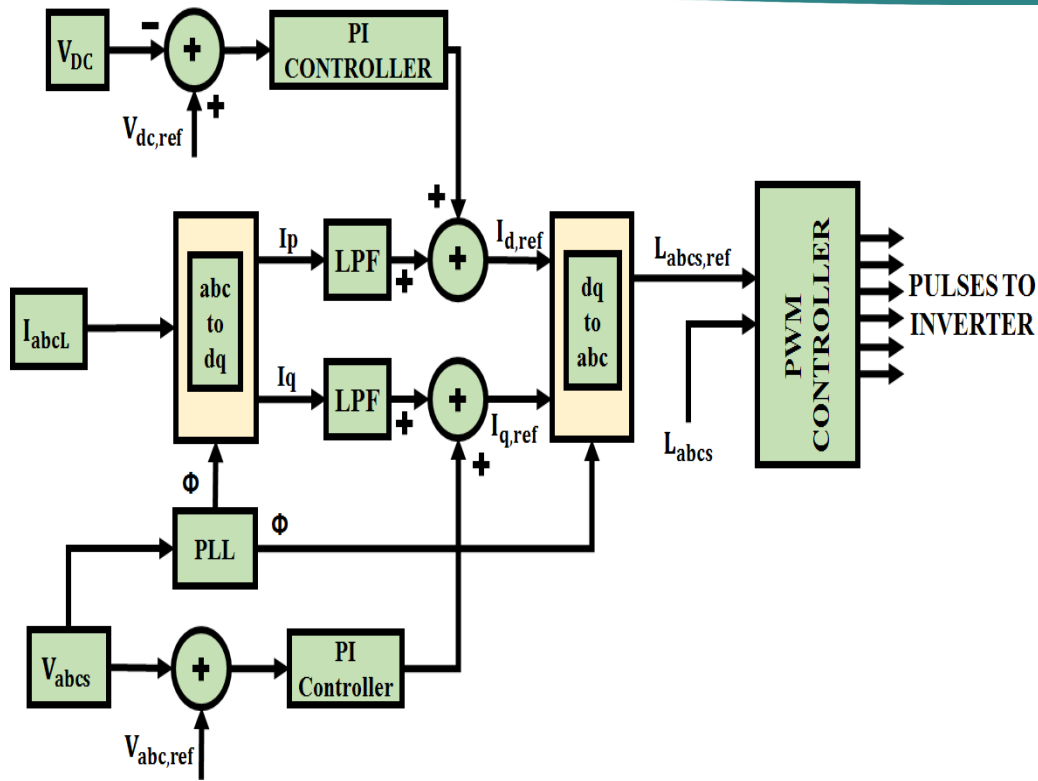


Figure 6. Shunt control approach

(3) Selection Procedure: Choose individuals with high fitness function values, crossover operations and run mutation and compare the new individuals with the previous generation to compare the fitness function values of two individuals. If the former exceeds the latter in size, move on to the next iteration; otherwise, stay the same.

Figure 5 indicates the flow chart representation of the ICS Algorithm. The proposed ICS fine-tuned the parameter with the best optimal solution.

Modelling of UPQC

Generally, the UPQC consists of two main converters: shunt and dc converters. These two converters maintain a common DC link with a renewable energy source, which helps UPQC work to enhance the PQ. The UPQC serves as a power quality mitigation and conditioning device for PV integration. The shunt converter injects current to reduce harmonics caused by nonlinear load. In order to provide and inject voltage in series with line to the distortion-free voltage at the load terminal, the UPQC series converter is used in voltage control mode. Because of its excellent rapid performance and minimal computational effort, a DQ theory-based control, a time domain approach is adopted.

Shunt Control Technique

The improved DQ control technique is shown in Figure 6. The power supply current (I_{sa}, I_{sb}, I_{sc}), power supply voltage (V_{sa}, V_{sb}, V_{sc}), load current (I_{La}, I_{Lb}, I_{Lc}),

and DC link voltage (V_{dc}) are the first signals sensed in this control approach.

Eventually, change the load current from an abc-frame to a dq-frame using equation (18).

$$\begin{bmatrix} i_d \\ i_q \\ i_o \end{bmatrix} = \frac{2}{3} \begin{bmatrix} \cos \phi & -\sin \phi \frac{1}{2} \\ \cos(\phi - \frac{2\pi}{3}) & -\sin(\phi - \frac{2\pi}{3}) \frac{1}{2} \\ \cos(\phi + \frac{2\pi}{3}) & \sin(\phi - \frac{2\pi}{3}) \frac{1}{2} \end{bmatrix} \begin{bmatrix} i_{1a} \\ i_{1b} \\ i_{1c} \end{bmatrix} \dots (18)$$

To produce a constant DC component, the filter lets these i_d and i_q components through. The reference current, i_{dref} , is obtained by combining i_{ddc} with the loss component obtained by adjusting the DC link error signal. Equations (19) and (20) provide the loss component and the direct axis reference current.

$$i_{1n} = i_{1(n-1)} + k_{pd}(V_{de(n)} - V_{de(n-1)} + k_{id}V_{de(n)}) \dots (19)$$

$$i_d^* = i_1 + i_{ddc} \dots (20)$$

The reference current i_{qref} is obtained similarly by combining i_{qdc} with the tuning power supply voltage error signal, as illustrated in equation (21). The final reference currents provided by equation are these reference i_{dref} and i_{qref} currents are converted back to the abc-reference system via the inverse Park Transformation (22).

$$i_{qrn} = i_{qr(n-1)} + k_{pq}(V_{te(n)} - V_{te(n-1)} + k_{id}V_{te(n)}) \dots (21)$$

$$i_q^* = i_{qr} + i_{qdc} \dots\dots\dots (22)$$

The PWM controller generates gate pulses based on the difference between the reference source current and the actual source current. Using the flash pulse as a basis, the current injects into the PCC to reduce harmonics and compensate for the necessary reactive power.

Series Control Technique

The series controller aims to lessen voltage sags, surges, and interruptions. Moreover, voltage harmonics are compensated appropriately. In Figure 7, the suggested series voltage controller is displayed. First, transform the source voltage into the synchronous dq0 reference frame using the following equation.

$$V_{s1}^{dq0} = T_s * V_{s1}^{abc} = V_{S1p} + V_{S1n} + V_{S10} + V_{S1h} \dots\dots (23)$$

It comprises positive, negative, zero sequence, and harmonic components, respectively, designated as $V_{S1p}, V_{S1n}, V_{S10}$ and V_{S1h} . The load voltage's dq0 reference system is developed to obtain the sinusoidal voltage.

$$V_{L1}^{dq0} = T_s * V_{s1}^{abc} = \begin{bmatrix} U_m \\ 0 \\ 0 \end{bmatrix} \dots\dots\dots (24)$$

$$\text{Where } T_s = \begin{bmatrix} \cos \phi & -\sin \phi \frac{1}{2} \\ \cos(\phi - \frac{2\pi}{3}) & -\sin(\phi - \frac{2\pi}{3}) \frac{1}{2} \\ \cos(\phi + \frac{2\pi}{3}) & \sin(\phi - \frac{2\pi}{3}) \frac{1}{2} \end{bmatrix} \dots\dots\dots (25)$$

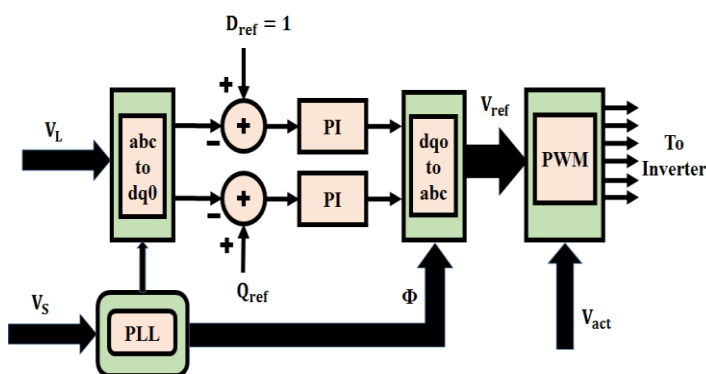


Figure 7. Series control approach

This formula yields the compensation dq0 reference frame voltage:

$$V_{dq0}^{ref} = V_{s1}^{dp0} - V_{L1}^{dp0} \dots\dots\dots (26)$$

The abc-reference frame continues to be converted from this compensating voltage. Produce gate pulses using PWM to produce compensation for load voltage fluctuations. As a result, an effective ICS with ANFIS

MPPT for PV-UPQC system using an Interleaved Zeta converter achieves improved PQ issues.

Results and Discussion

An effective Improved CS optimization is introduced for the PV-UQPC system using the interleaved zeta converter discussed in this work. To deliver the power required by the shunt and series compensation, the PV system is connected to the DC link of UPQC. This part validates the suggested control approach with MATLAB and discusses the outcomes in-depth. Table 1 displays the parameters used to develop the suggested work.

Table 1. Parameter Specification

Parameter	Specification
Solar PV System	
Series connected solar PV cells	36
Open Circuit Voltage	12V
Short Circuit Current	8.33A
Peak Power	10KW, 10 Panels
Interleaved Zeta Converter	
$L_1, L_2, L_3 \& L_4$	$2 * (350\mu H - 6A) + 2 * (350\mu H - 2A)$
$C_1, C_2, C_3 \& C_4$	47uF
$C_{O1} \& C_{O2}$	180u
C_{in}	470 uF

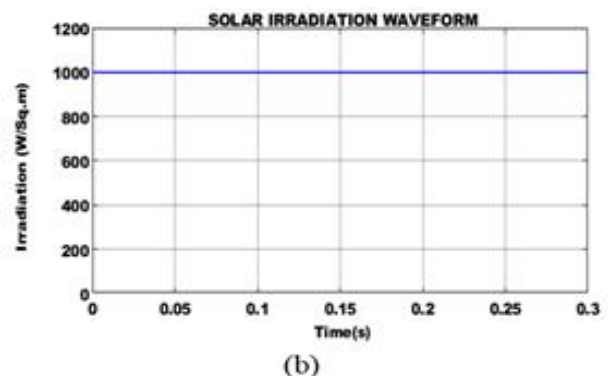
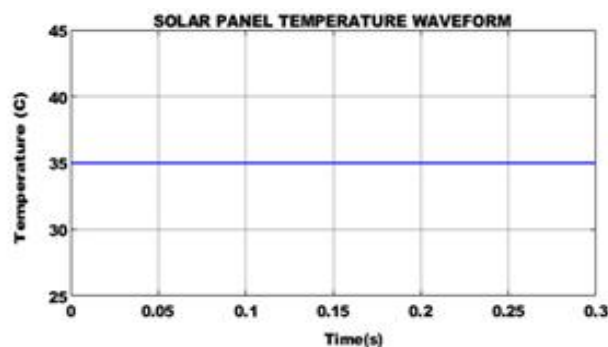


Figure 8. Solar panel (a) Temperature (b) Irradiation

To verify the functionality of the interleaved Zeta converter, a PV system powered by renewable energy is linked to the interleaved converter. It is obvious from the waveform observation in Figure 8(a) that the temperature

remained constant at 35°C, and this information may be used to analyse the effectiveness of the suggested technique in addressing the intermittent nature of the PV system. Similarly, Figure 8 (b) demonstrates that an irradiation of 1000 W/Sq.m PV system is attained.

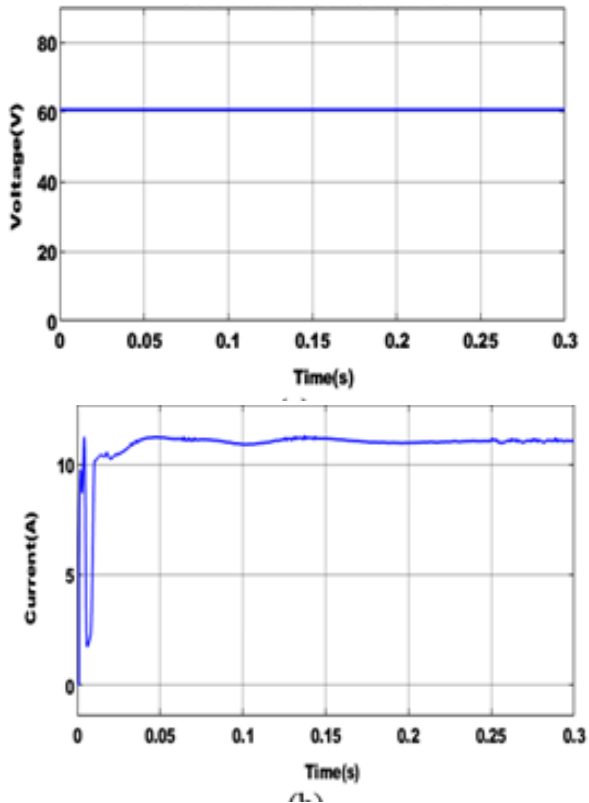
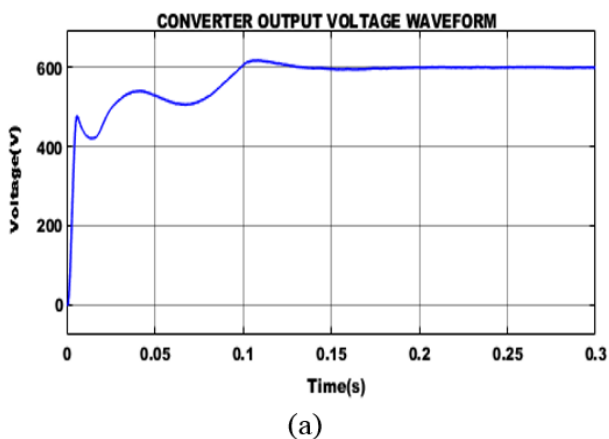
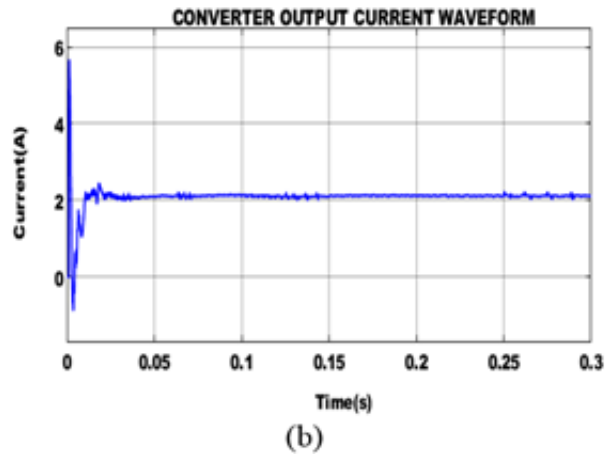


Figure 9. Solar panel (a) Voltage (b) Current

Figure 8 indicates the waveform representation for Voltage and irradiation of PV system. From the graph, it is noted that the constant voltage 60V is maintained. Similarly, the current obtains a peak value and after 0.2 sec it maintains a constant value of 12 A.



(a)

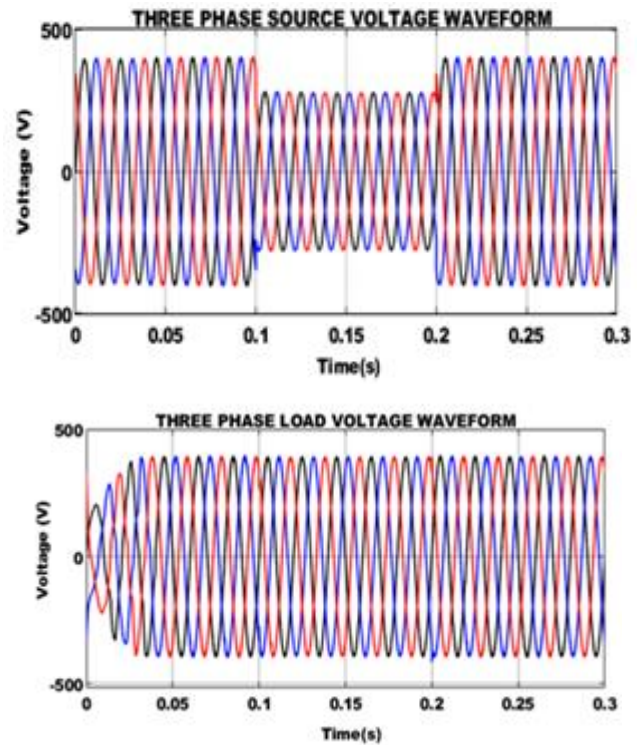


(b)

Figure 10. Output of Interleaved Zeta converter (a) Voltage (b) Current

In Figure 9(a), the waveforms for the Interleaved Zeta converter's voltage output are displayed. The converter's output voltage of 600V is reached in 0.15 seconds, even with some initial variations. The output current is also dynamic in Figure 7(b) until 0.5s, at which point a constant value of 2A is maintained.

Case 1: FOR STEP MAGNITUDE (-0.3)



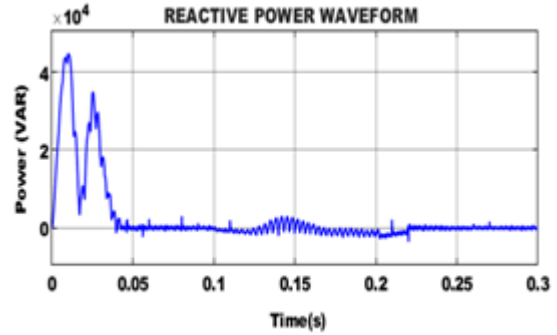
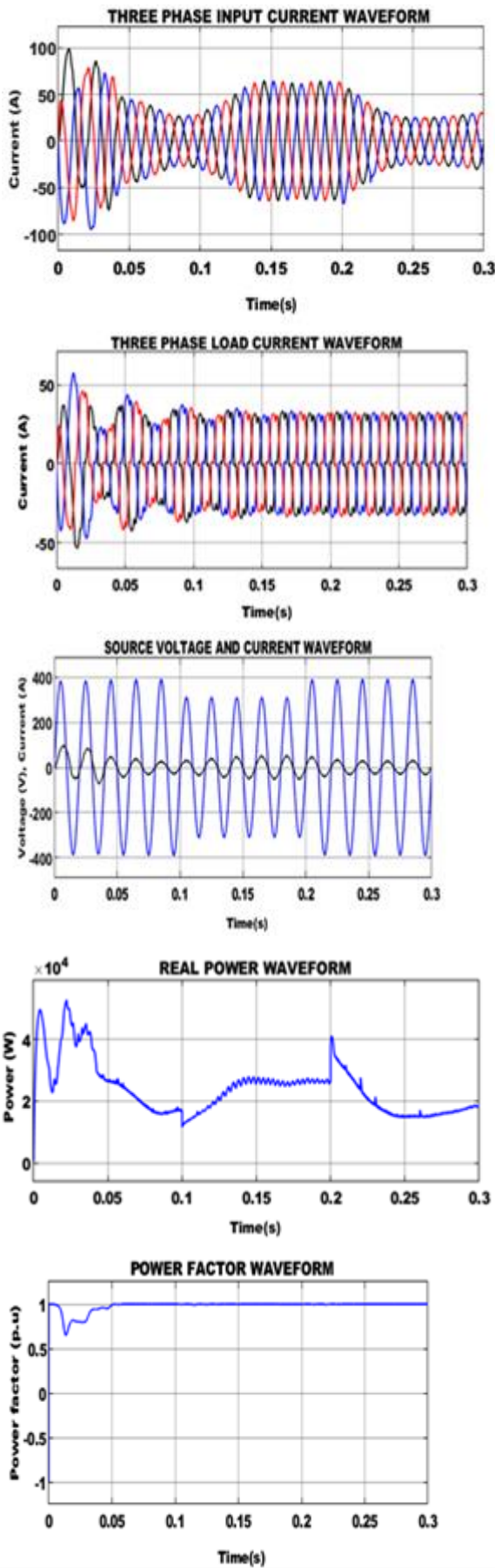
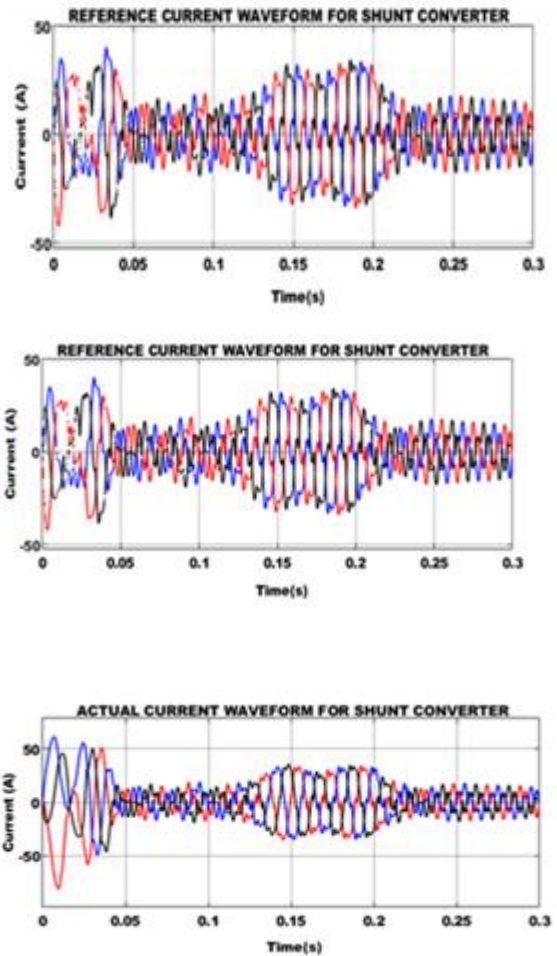


Figure 11. Simulation results for voltage sag condition

Case 1 assesses the feasibility and effectiveness of the proposed PV-UPQC under voltage sag conditions. The source voltage is initially 400V, and between the intervals of 0.1 and 0.2 seconds, a voltage sag of magnitude -0.3 p.u is induced. The input current increases to 55A during this time, as seen in Figure 10. The resulting waveform also shows the analysis of the source current and source voltage under voltage sag by considering into reference a single phase. Moreover, at 0.05 seconds a unity power factor is attained. Due to the robust compensation abilities of the series compensator, the stability of the load side voltage is maintained over the voltage sag in the grid side. The suggested method also successfully reduces reactive power.



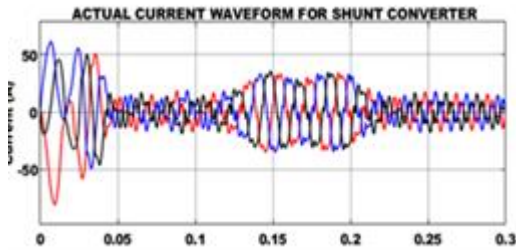


Figure 12. The waveform of shunt and series compensator for voltage sag condition

Figure 11 shows the reference current signal for the shunt compensator that is produced using the PI controller-assisted dq theory. Similarly, the reference voltage for the series compensator signal is produced with the aid of the dq theory. The waveform also shows the actual voltage and current waveforms for the shunt and series compensators. As a result of using the designed PV-UPQC, the system's current and voltage quality issues are rectified.

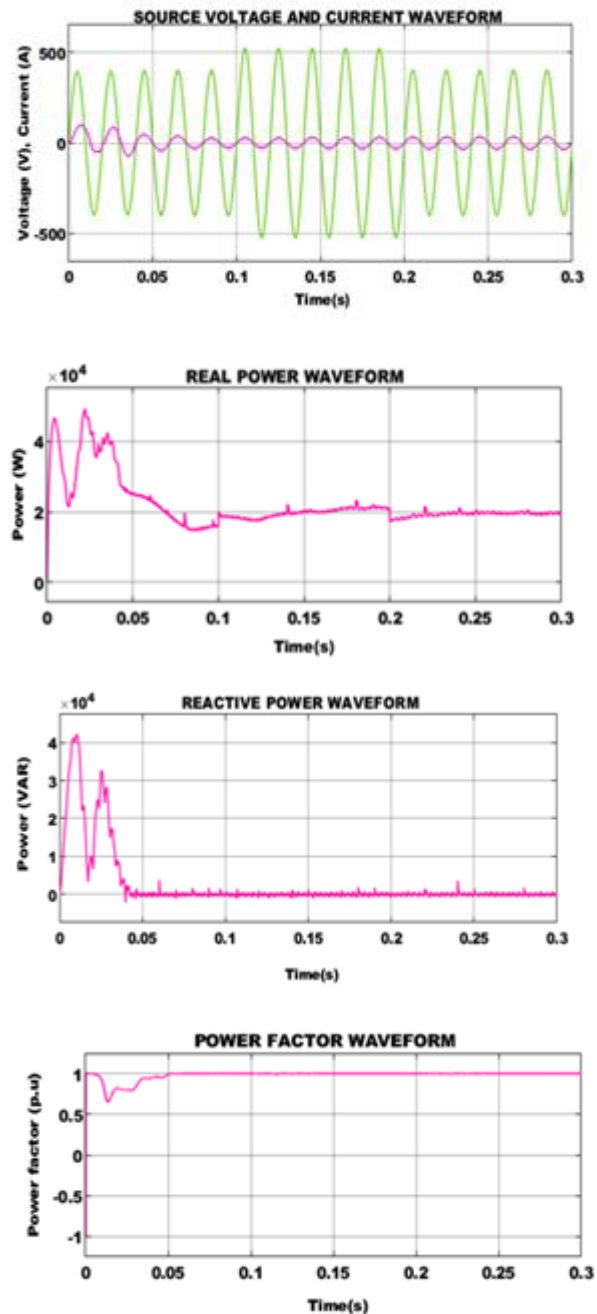
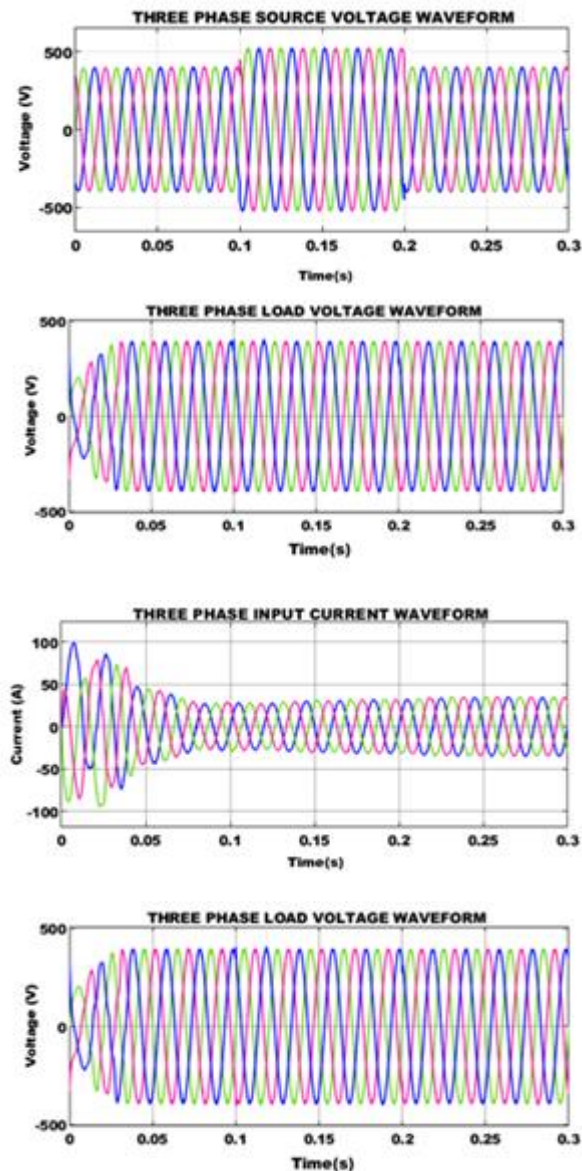


Figure 13. Simulation results for voltage swell condition

Case 2 evaluates the PV-UPQC's dynamic operation in the presence of voltage swell, and Figure 12 shows the relevant waveforms. In case 2, between 0.1 and 0.2 seconds, a voltage swell of magnitude 0.3p.u is seen in the source voltage waveform. The next waveform shows the corresponding input current waveform, in which magnitude decreases in response to the voltage swell. The waveforms for a single phase of the source voltage and current under the voltage swell condition are also considered. Similar to case 1, case 2 a unity power factor is achieved. The excellent compensating capability of the series and shunt compensators of the proposed UPQC enables the maintenance of a constant and stable voltage

and current on the load side. Figure 12 also displays the real and reactive power waveforms. Figure 13 shows the reference voltage and current waveforms that are used to rectify PQ concerns.

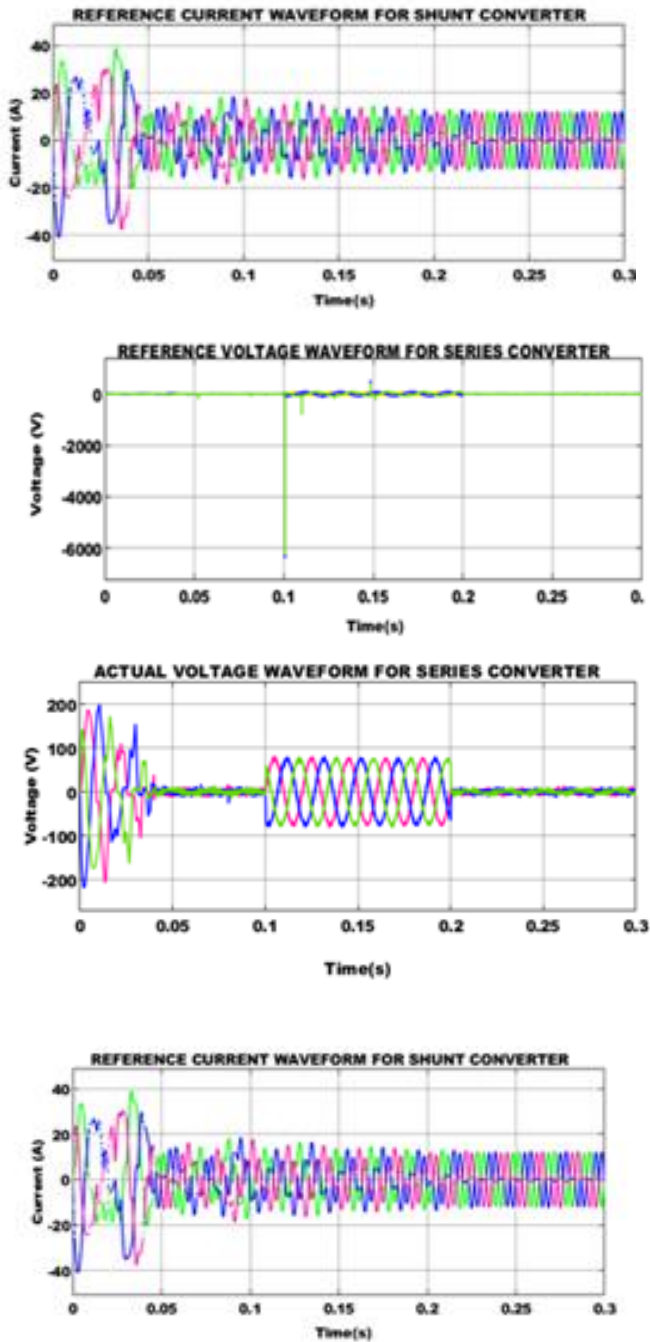


Figure 14. Waveform of shunt and series compensator for voltage swell condition

Figure 15 shows the comparison graphs used to evaluate the effectiveness of DC-DC converters in terms of voltage gain, efficiency, and tracking efficiency. The suggested ICS with ANFIS MPPT has the best efficiency, approximately 96%, compared to other methods. Similarly the THD waveform is seen in Figure 14. The Interleaved Zeta values' voltage gain, efficiency and THD are 1:10, 97.2% and 2.01%, respectively.

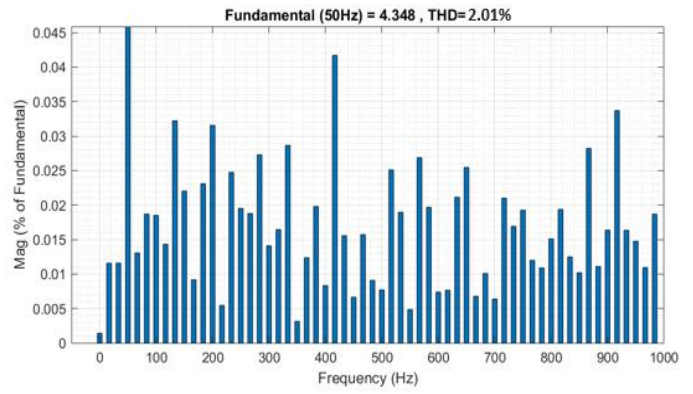
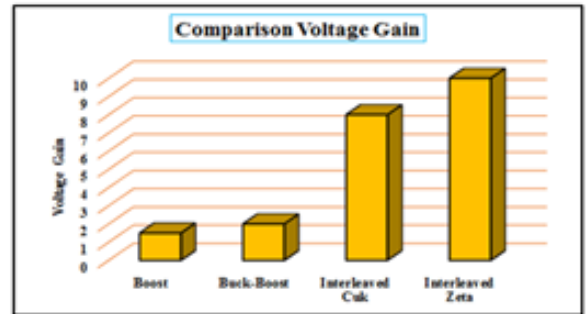
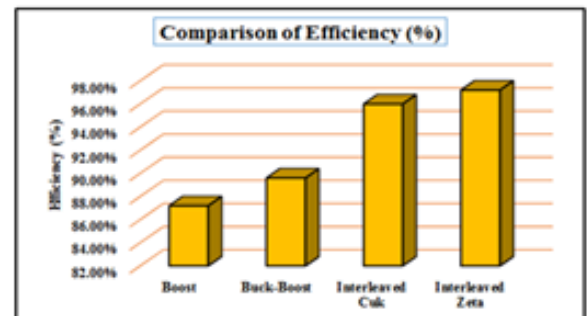


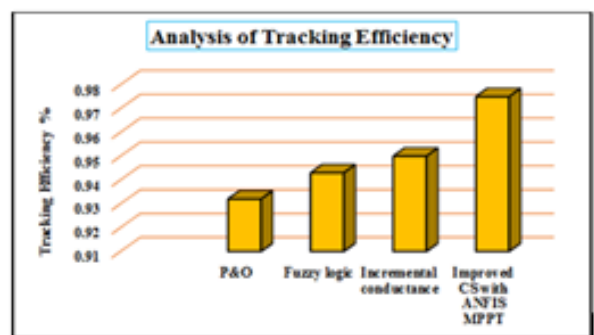
Figure 15. THD Waveform



(a)



(b)



(c)

Conclusion

The RES are crucial to producing the electrical power in grid-coordinated PV systems. Because of the RES connection with the grid, the PQ problems show a drastic increase. Due to this poor power quality, the grid has to deal with load shedding, fluctuations in harmonics, voltage swell & sag and other difficulties that impact the

system. The PV integrated UPQC is proposed in this work to overcome the issues mentioned earlier. With the help of Interleaved Zeta converter the high ripples are eliminated. In order to track the maximized power from PV, the ANFIS MPPT is introduced, which is effectively tuned by the employment of the improved CS algorithm. The UQPC's series and shunt compensators lessen voltage sag, swell, and harmonics. The coordinated dq theory and PI controller are introduced to enhance the performance of the UPQC system. The proposed PV-UPQC is evaluated and the simulation results are obtained in MATLAB platform. According to the results, the interleaved zeta converter has an outstanding efficiency of 97.2% with minimized THD value of 2.01%.

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Conflict of Interest

Nil

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